

WP934SA/IYGD5V

T-1 (3mm) Tri-Level Circuit Board Indicator



DESCRIPTIONS

- The High Efficiency Red source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Orange Light Emitting Diode
- The Yellow source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Yellow Light Emitting Diode
- The Green source color devices are made with Gallium Phosphide Green Light Emitting Diode
- · Electrostatic discharge and power surge could damage the LEDs
- · It is recommended to use a wrist band or anti-electrostatic glove when handling the LEDs
- · All devices, equipments and machineries must be electrically grounded

FEATURES

- · Pre-trimmed leads for pc mounting
- · Black case enhances contrast ratio
- · High reliability life measured in years
- Housing UL rating: 94V-0
- Housing material: Type 66 nylon
- 5V internal resistor
- RoHS compliant

APPLICATIONS

- Status indicator
- Illuminator
- Signage applications

SELECTION GLUDE

- · Decorative and entertainment lighting
- · Commercial and residential architectural lighting

ATTENTION

Observe precautions for handling electrostatic discharge sensitive devices



	Emitting Color (Material)	Lens Type	Iv (mcd) V = 5V ^[2]		Viewing Angle ^[1]	
Part Number			Min.	Тур.	201/2	
	High Efficiency Red (GaAsP/GaP)	Red Diffused	12	25	50°	
			*6	*14		
WP934SA/IYGD5V		Yellow Diffused	7	15	=0°	
WF9545A/ITGD5V	Yellow (GaAsP/GaP)	reliow Diffused	*7	*15	50°	
	Green (GaP)	Green Diffused	12	25	50°	
			*12	*25	50	

Notes

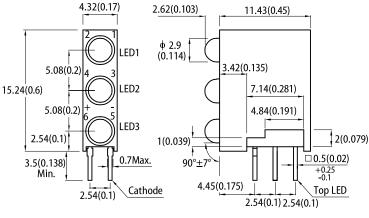
01/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.

2. Luminous intensity / luminous flux: +/-15%. * Luminous intensity value is traceable to CIE127-2007 standards

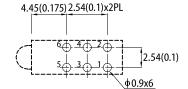


PACKAGE DIMENSIONS

LED1 : Red
LED2 : Yellow
LED3 : Green



Recommended PCB Layout



Notes:

- 1. All dimensions are in millimeters (inches)
- Tolerance is ±0.25(0.01") unless otherwise noted.
 Lead spacing is measured where the leads emerge from the package
- The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

ELECTRICAL / OPTICAL CHARACTERISTICS at T_A=25°C

Deventer	Queen bal	Emitting Color	Value		11
Parameter	Symbol	Emitting Color	Typ. Max.		Unit
Wavelength at Peak Emission V_F = 5V	λ_{peak}	High Efficiency Red Yellow Green	627 590 565	-	nm
Dominant Wavelength V_F = 5V	λ_{dom} ^[1]	High Efficiency Red Yellow Green	617 588 568	-	nm
Spectral Bandwidth at 50% Φ REL MAX V _F = 5V	Δλ	High Efficiency Red Yellow Green	45 35 30	-	nm
Forward Current $V_F = 5V$	I _F	High Efficiency Red Yellow Green	13 13 11.5	17.5 17.5 17.5	mA
Reverse Current (V _R = 5V)	I _R	High Efficiency Red Yellow Green	-	10 10 10	μΑ
Temperature Coefficient of λ_{peak} V_F = 5V , -10°C $\leq T \leq 70^{\circ}C$	TC_{\lambdapeak}	High Efficiency Red Yellow Green	0.13 0.12 0.1	-	nm/°C
Temperature Coefficient of λ_{dom} V_F = 5V , -10°C $\leq T \leq 70^{\circ}C$	TC _{λdom} High Efficiency Red Yellow Green		0.06 0.07 0.06	-	nm/°C

Notes:

The dominant wavelength (λd) above is the setup value of the sorting machine. (Tolerance λd : ±1nm.)
 Wavelength value is traceable to CIE127-2007 standards.
 Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

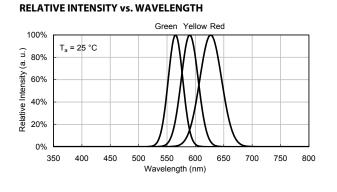
ABSOLUTE MAXIMUM RATINGS at T_A=25°C

Parameter	Symbol	Value			Unit
Falalletei		High Efficiency Red	Yellow	Green	
Power Dissipation	P _D	85	85	85	mW
Reverse Voltage	V _R	5			V
Junction Temperature	Tj	125	110	110	°C
Operating Temperature	T _{op}	-40 to +70			°C
Storage Temperature	T _{stg}	-40 to +85			°C
Forward Voltage	V _F	6	6	6	V
Electrostatic Discharge Threshold (HBM)	-	3000	3000	3000	v
Thermal Resistance (Junction / Ambient)	R _{th JA} ^[1]	300	250	400	°C/W
Thermal Resistance (Junction / Solder point)	R _{th JS} ^[1]	100	100	135	°C/W
Lead Solder Temperature [2]		260°C For 3 Seconds			
Lead Solder Temperature [3]		260°C For 5 Seconds			

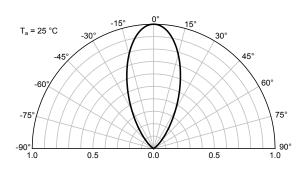
Notes:

Notes: 1. R_{In JA, RLn JS} Results from mounting on PC board FR4 (pad size ≥ 16 mm² per pad). 2. 2mm below package base. 3. 5mm below package base. 4. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

TECHNICAL DATA



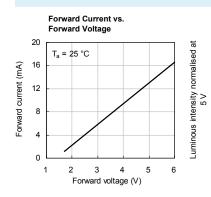
SPATIAL DISTRIBUTION

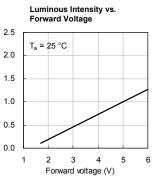


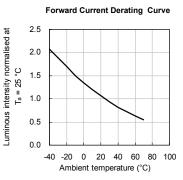
HIGH EFFICIENCY RED

at

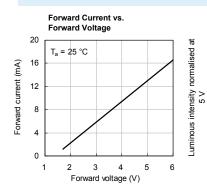
Luminous i

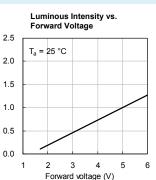


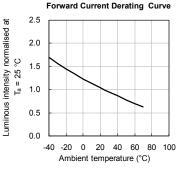


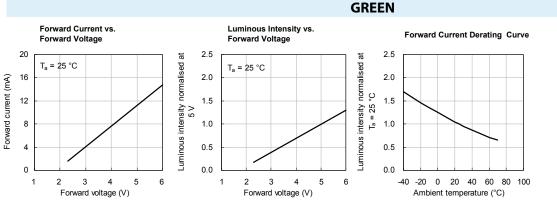


YELLOW



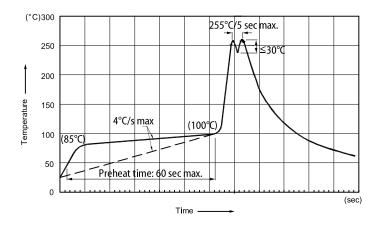






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RECOMMENDED WAVE SOLDERING PROFILE



- Notes: 1. Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C
- 2. Peak wave soldering temperature between 245°C ~ 255°C for 3 sec (5 sec max).
- Do not apply stress to the epoxy resin while the temperature is above 65°C.
 Fixtures should not incur stress on the component when mounting and during soldering process.
- SAC 305 solder alloy is recommended.
 No more than one wave soldering pass.

PRECAUTIONS

Storage Conditions

- 1. Avoid continued exposure to the condensing moisture environment and keep the product away from rapid transitions in ambient temperature.
- 2. LEDs should be stored with temperature \leq 30°C and relative humidity < 60%.
- 3. Product in the original sealed package is recommended to be assembled within 72 hours of opening. Product in opened package for more than a week should be baked for 30 (+10/-0) hours at 85 ~ 100°C.

LED Mounting Method

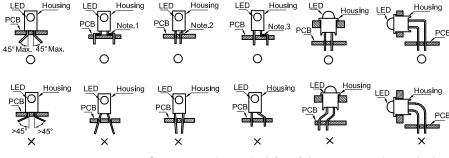
1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead-forming may be required to insure

the lead pitch matches the hole pitch. Refer to the figure below for proper lead

forming procedures.

Note 1-3: Do not route PCB trace in the contact area between the

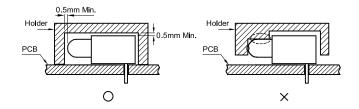
leadframe and the PCB to prevent short-circuits.



○ " Correct mounting method " x " Incorrect mounting method

Lead Forming Procedures

- 1. During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during soldering.
- 2. The tip of the soldering iron should never touch the lens epoxy.
- 3. Through-hole LEDs are incompatible with reflow soldering.
- 4. If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.



E: XXXX

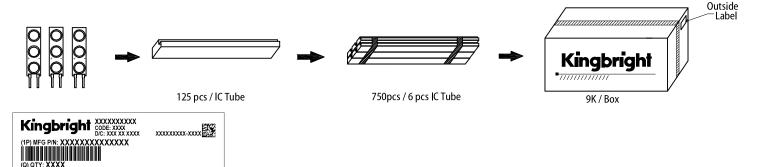
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COO: CN

18. D.S.

WP934SA/IYGD5V

PACKING & LABEL SPECIFICATIONS



PRECAUTIONARY NOTES

- 1. 2.
- The information included in this document reflects representative usage scenarios and is intended for technical reference only. The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to the latest datasheet for the updated specifications.
- When using the products referenced in this document, please make sure the product is being operated within the environmental and electrical limits specified in the datasheet. If customer usage exceeds the specified limits, Kingbright will not be responsible for any subsequent issues. The information in this document applies to typical usage in consumer electronics applications. If customer's application has special reliability requirements or have life-threatening 3.
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- 5. 6. Notes