



190319499 Availability of Si5332 rev1.2 Datasheet

PCN Issue Date: 3/19/2019

Effective Date: 6/25/2019

PCN Type: Datasheet

Description of Change

Silicon Labs is pleased to announce the release of revision1.2 of the Si5332 datasheet.

Reason for Change

The following changes were made:
Update QFN and LGA package thermal specifications based on JEDEC board analysis
Clarify certain specifications to be consistent throughout the datasheet
Notification that Si5332 meets PCIe Gen5 specifications.

Impact on Form, Fit, Function, Quality, Reliability

There is no impact on form, fit, quality or reliability.

The following parameters have been updated in revision1.2 of the Si5332 datasheet:
Updated QFN package thermal specifications
Added LGA package thermal specifications
Updated PCIe Gen2/3/4 jitter specifications based on results using PCIe Clock Jitter Tool version3.0
Added PCIe Gen5 jitter specifications
Clarified references to QFN (A/B/C/D grades) vs LGA (E/F/G/H grades)
Corrected all references to VDDO to +/-5%
Add note stating all core voltages must be 3.3V for in-system programming through I2C
Clarified reference crystal ESR specification
Updated reference crystal max loading capacitance value for 16-30MHz crystals

Product Identification

Existing Part #
Si5332A-D-GM1
Si5332A-D-GM2
Si5332A-D-GM3
Si5332B-D-GM1
Si5332B-D-GM2
Si5332B-D-GM3
Si5332C-D-GM1
Si5332C-D-GM2
Si5332C-D-GM3
Si5332D-D-GM1
Si5332D-D-GM2
Si5332D-D-GM3
Si5332E-D-GM1
Si5332E-D-GM2
Si5332E-D-GM3
Si5332F-D-GM1
Si5332F-D-GM2
Si5332F-D-GM3
Si5332G-D-GM1
Si5332G-D-GM2
Si5332G-D-GM3

Si5332H-D-GM1
Si5332H-D-GM2
Si5332H-D-GM3
Si5332ADxxxxx-GM1
Si5332ADxxxxx-GM2
Si5332ADxxxxx-GM3
Si5332BDxxxxx-GM1
Si5332BDxxxxx-GM2
Si5332BDxxxxx-GM3
Si5332CDxxxxx-GM1
Si5332CDxxxxx-GM2
Si5332CDxxxxx-GM3
Si5332DDxxxxx-GM1
Si5332DDxxxxx-GM2
Si5332DDxxxxx-GM3
Si5332ExxxxxD-GM1
Si5332ExxxxxD-GM2
Si5332ExxxxxD-GM3
Si5332FxxxxxD-GM1
Si5332FxxxxxD-GM2
Si5332FxxxxxD-GM3
Si5332GxxxxxD-GM1
Si5332GxxxxxD-GM2
Si5332GxxxxxD-GM3
Si5332HxxxxxD-GM1
Si5332HxxxxxD-GM2
Si5332HxxxxxD-GM3

Last Date of Unchanged Product: 6/25/2019

Qualification Samples

N/A

Customer Response

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at <http://www.silabs.com>.

Customers may approve early PCN acceptance by emailing approval, along with PCN # to PCNEarlyAcceptance@silabs.com

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Qualification Data

N/A



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