



PIC16C57 → PIC16C57C Migration

DEVICE MIGRATIONS

This document is intended to describe the functional differences and the electrical specification differences that are present when migrating from one device to the next.

Note: This device has been designed to perform to the parameters of its data sheet. It has been tested to an electrical specification designed to determine its conformance with these parameters. Due to process differences in the manufacture of this device, this device may have different performance characteristics than its earlier version. These differences may cause this device to perform differently in your application than the earlier version of this device.

Table 1 shows the considerations that must be taken into account when migrating from the PIC16C57 to the PIC16C57C.

TABLE 1: PIC16C57 → PIC16C57C DIFFERENCES

Functional Differences				
No.	Difference	H/W	S/W	Prog.
1	Master Clear Filter added, PIC16C57C. See Electrical Specification #30	✓	—	—
2	Programming algorithm change, PIC16C57C uses a new programming algorithm	—	—	✓
3	Oscillator configuration bits are user selectable on the PIC16C57C	—	✓	—

Electrical Specification Differences

Parm. No.	Sym.	Characteristic	PIC16C57 Data Sheet			PIC16C57C Data Sheet			Units	Conditions	
			Min	Typ	Max	Min	Typ	Max			
	VDD	Supply Voltage									
		XT, RC Options	3.0	—	6.25	3.0	—	5.5	V	Note 4	
		LP Option	2.5	—	6.25	2.5	—	5.5	V		
		HS option	4.5	—	5.5	4.5	—	5.5	V		
		XT, RC Opt. Extended	3.25	—	6.0	3.0	—	5.5	V	Note 4	
LP Option Extended	2.5	—	6.0	3.0	—	5.5	V				
	IDD	Supply Current									
		XT and RC options	—	1.8	3.3	—	1.8	2.4	mA	Note 1	
		HS option	—	4.8	10	—	4.5	16	mA	Note 2	
		LP Option, Commercial	—	15	32	—	14	32	μA	Note 3	
		LP Option, Industrial	—	15	40	—	17	40	μA	Note 3	
	IPD	Power Down Current									
		Industrial	—	4.0	14	—	4.0	14	μA	VDD=3.0V WDT Enabled	
			—	0.6	12.0	—	0.25	5.0	μA	WDT Disabled	
		Extended	—	5.0	22	—	4.5	22	μA	WDT Enabled	
			—	0.8	18	—	0.3	18	μA	WDT Disabled	
	VIL	Input Low Voltage									
		I/O Ports	VSS	—	0.2 VDD						4.0V < VDD ≤ 5.5V For all VDD
					VSS	—	0.8	V		4.5V < VDD ≤ 5.5V	
					VSS	—	0.15 VDD	V		Otherwise	
	VIH	Input High Voltage									
		I/O Ports	2.0	—	VDD						4.0V < VDD ≤ 5.5V For all VDD
			0.45 VDD	—	VDD						4.5V < VDD ≤ 5.5V
					2.0	—	VDD	V		Otherwise	
					0.25 VDD + 0.8V	—	VDD	V			

Note 1: FOSC=4.0MHz, VDD=5.5V

2: FOSC=20MHz, VDD=5.5V

3: FOSC=32kHz, VDD=3.0V, WDT disabled

4: The LP oscillator option is specified for the PIC16C55 up to 40kHz.

Note: The user should verify that the device oscillator starts and performs as expected. Adjusting the loading capacitor values and /or the oscillator mode may be required.

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- The PICmicro family meets the specifications contained in the Microchip Data Sheet.
- Microchip believes that its family of PICmicro microcontrollers is one of the most secure products of its kind on the market today, when used in the intended manner and under normal conditions.
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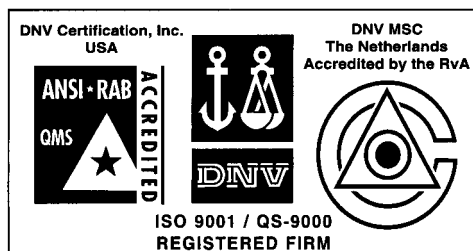
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