

Vishay Siliconix

## N-Channel 30-V (D-S) MOSFET

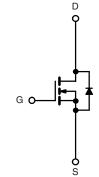
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)		
30	0.011 at V <sub>GS</sub> = 10 V	12		
	0.0145 at V <sub>GS</sub> = 4.5 V	9.8		

#### FEATURES

- Halogen-free According to IEC 61249-2-21
  Available
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> Tested
- 100 % UIS Tested

#### **APPLICATIONS**

- Notebook PC
  - Core - System Power





FREE Available

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SO-8

Top View

Ordering Information: Si4688DY-T1-E3 (Lead (Pb)-free) Si4688DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	T <sub>A</sub> = 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	30		V
Gate-Source Voltage		V <sub>GS</sub>	± 20		
	T <sub>A</sub> = 25 °C	– I <sub>D</sub>	12	8.9	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		9.5	7.1	
Pulsed Drain Current		I <sub>DM</sub>	40		А
Continuous Source Current (Diode Conduction) <sup>a</sup>		۱ <sub>S</sub>	2.3	1.3	
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	20		
Avalanche Energy	L = 0.1 MH	E <sub>AS</sub> 20		mJ	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	– P <sub>D</sub>	2.5	1.4	W
	T <sub>A</sub> = 70 °C		1.6	0.9	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum lunction to Amhienta	t ≤ 10 s	R <sub>thJA</sub>	43	50	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		73	90	°C/W
Maximum Junction-to-Foot (Drain)	Steady State		19	25	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

## Si4688DY

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1.0		3.0	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current	1	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1		
	DSS	$V_{DS}$ = 30 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 °C			5	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5$ V, $V_{GS}$ = 10 V	30			А	
Drain-Source On-State Resistance <sup>a</sup>	D			0.009	0.011	Ω	
	R <sub>DS(on)</sub>			0.012	0.0145		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 12 A		32		S	
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{S} = 2.3 \text{ A}, V_{GS} = 0 \text{ V}$		0.76	1.1	V	
Dynamic <sup>b</sup>				1			
Input Capacitance	C <sub>iss</sub>			1580		pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$		295			
Reverse Transfer Capacitance	C <sub>rss</sub>			140			
Tatal Cata Charge	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 5 \text{ V}, I_{D} = 12 \text{ A}$		13.2	20		
Total Gate Charge	•			25.4	38	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = 15 V, $V_{GS}$ = 10 V, $I_D$ = 12 A		5.3			
Gate-Drain Charge	Q <sub>gd</sub>			4.3		1	
Gate Resistance	R <sub>g</sub>		0.9	1.8	2.7	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			13	20		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 15 $\Omega$		10	15	]	
Turn-Off Delay Time	t <sub>d(off)</sub>	${\rm I}_{\rm D} \cong$ 1 A, ${\rm V}_{\rm GEN}$ = 10 V, ${\rm R}_{\rm g}$ = 6 $\Omega$		33	50	ns	
Fall Time	t <sub>f</sub>			10	15		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 2.3 A, dl/dt = 100 A/μs		25	40	1	

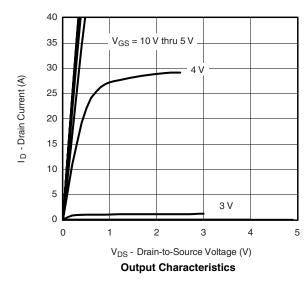
Notes:

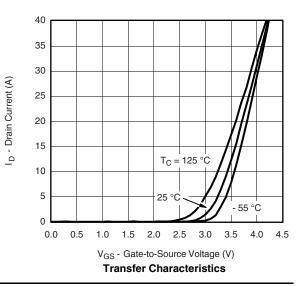
a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





Si4688DY VISHAY Vishay Siliconix TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted 0.020 2000 Ciss 0.016 1600 V<sub>GS</sub> = 4.5 V  $\mathsf{R}_{\mathsf{DS}(\mathsf{on})}$  - On-Resistance  $(\Omega)$ C - Capacitance (pF) 0.012 1200  $V_{GS} = 10 V$ 0.008 800 0.004 400 Coss Crss 0.000 0 5 0 5 10 15 20 25 30 0 10 15 20 25 I<sub>D</sub> - Drain Current (A) V<sub>DS</sub> - Drain-to-Source Voltage (V) **On-Resistance vs. Drain Current** Capacitance 10 1.6 V<sub>DS</sub> = 15 V I<sub>D</sub> = 12 A  $V_{GS} = 10 V$  $I_D = 12 A$ V<sub>GS</sub> - Gate-to-Source Voltage (V) 8 1.4 R<sub>DS(on)</sub> - On-Resistance (Normalized) 1.2 6 4 1.0 0.8 2 0.6 0 0 5 10 15 20 25 - 50 - 25 0 25 50 75 100 125 Qg - Total Gate Charge (nC) T<sub>J</sub> - Junction Temperature (°C) **Gate Charge On-Resistance vs. Junction Temperature** 30 0.05 0.04  $R_{DS(on)}$  - On-Resistance ( $\Omega$ ) T<sub>J</sub> = 150 °C Is - Source Current (A) 10 I<sub>D</sub> = 12 A 0.03 0.02  $T_J = 25 \ ^\circ C$ 0.01 0.00 1 0.0 0.2 0.4 0.6 0.8 1.0 0 2 4 6 8 1.2 V<sub>SD</sub> - Source-to-Drain Voltage (V) V<sub>GS</sub> - Gate-to-Source Voltage (V)

Source-Drain Diode Forward Voltage

On-Resistance vs. Gate-to-Source Voltage

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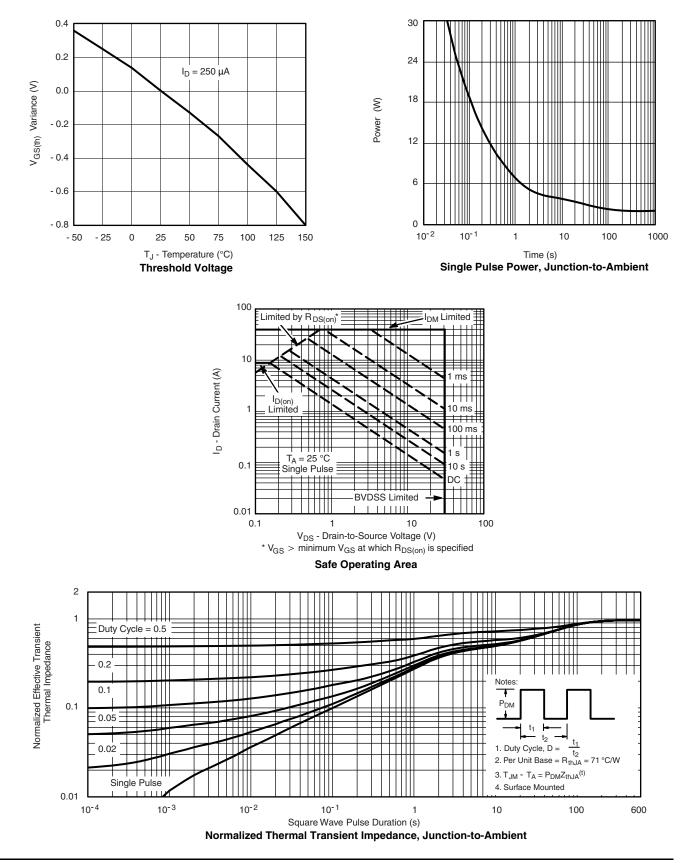
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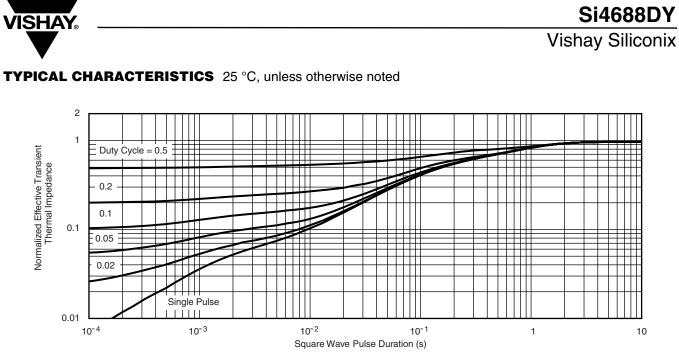
## Si4688DY

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# VISHAY.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?69996">www.vishay.com/ppg?69996</a>.



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