										_
PCN Number: 2016		20160630000B				F	PCN Date:	Dec. 1, 2016		
Title	Title: Qualification of AMKOR P3 as Additional Assembly and Test Site for Select LSON-CLIP Package Devices									
Customer Contact: PCN Manager Dept: Quality Services										
Cha	nge Type:									
	Assembly S	Site			Design	Design Wafer Bump			p Site	
	Assembly P	rocess			Data S	Data Sheet			Wafer Bump Material	
	Assembly N	1aterial:	S		Part number change				Wafer Bump Process	
	Mechanical	Specifi	cation		Test Site				Wafer Fab Site	
	Packing/Sh	ipping/	Labeling		Test Pr	ocess			Wafer Fab Materials	
									Wafer Fab Process	
					PCN	l Details				
Des	cription of	Chang	e:							
and Mate	Texas Instruments Incorporated is announcing the qualification of AMKOR P3 as Additional Assembly and Test Site for select devices listed in the "Product Affected" Section. Current assembly sites and Material differences are as follows.									
	sembly Site	Assen	າbly Site Orig	gin	Assemb	ly Country Code			sembly Site Ci	
 	TI Clark		QAB			PHL	Ar		es City, Pampa	
	Amkor P3		AP3			PHL		В	Biñan, Laguna	
Mat	Material Differences:									
			+	Clar		AMKOR P3				
	Mold compo			862		101390791				
	lount comp Controller s		(95Pl	083 b/50			3			
				o/ ၁၁ PdAu						
Lead finish		INIF	uAu		Matte 311					
Test coverage, insertions, conditions will remain consistent with current testing and verified with test MQ.										
Reason for Change:										
Continuity of supply.										
Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):										
None										
Anticipated impact on Material Declaration										
	No Impact to the Material Declaration Material Declarations or Product Content reports are driven from production data and will be available following the production release. Upon production release the revised reports can be obtained from the TI ECO website.				roduction					
Cha	Changes to product identification resulting from this PCN:									

Revision B is to update the product ECAT value resulting from this PCN. We apologize for any inconvenience this may have caused.

Sample product shipping label (not actual product label)

Assembly Site:

TI-CLARK	Assembly Site Origin (22L)	ASO: QAB	ECAT: G4
			ECAT: E4
AMKOR P3	Assembly Site Origin (22L)	ASO: AP3	ECAT: G3
			ECAT: E3

Sample product shipping label to show code location (not actual product label)



ECAT: E4 = NiPdAu ECAT: E3 = Matte Sn

(Q) 2000 (D) 0336 (31T)LOT: 3959047MLA (4W) TKY(1T) 7523483SI2

(2P) REV: (V) 0033317 (20L) CSO: SHE (21L) CCO:USA (22L) ASO: MLA (23L) ACO: MYS

ASSEMBLY SITE CODES: TI-CLARK = I, AP3 = 3

Product Affected:

HPA01110DQPR	TPS53319DQPT	TPS53355DQPT	TPS56221BDQPT
HPA01111DQPR	TPS53353DQPR	TPS56121BDQPR	TPS56221DQPR
SN1109022DQPR	TPS53353DQPT	TPS56121BDQPT	TPS56221DQPT
TPS53318DQPR	TPS53355ADQPR	TPS56121DQPR	
TPS53318DQPT	TPS53355ADQPT	TPS56121DQPT	
TPS53319DQPR	TPS53355DQPR	TPS56221BDQPR	
TPS53318DQPT	TPS53355ADQPT	TPS56121DQPT	

Qualification ReportAmkor AP3 Phase 6 HPS DQP Clip QFN Offload from Clark to Amkor

Date: 06/30/2016

Product Attributes

Attributes	Qual Device: TPS53319DQP	Qual Device: TPS53355DQP	Qual Device: TPS56121BDQP	
Assembly Site	AP3 (AMKOR P3)	AP3 (AMKOR P3)	AP3 (AMKOR P3)	
Package Family	SON	SON	SON	
Flammability Rating	UL 94 V-0	UL 94 V-0	UL 94 V-0	
Wafer Fab Supplier	CFAB, MIH0 8	CFAB, DMOS5	CFAB, DMOS5	

Texas Instruments, Inc. PCN#20160630000B

Attributes	Qual Device:	Qual Device:	Qual Device:
	TPS53319DQP	TPS53355DQP	TPS56121BDQP
Wafer Fab Process	FET_NCH_LV_GEN2.0, LBC7	FET_NCH_LV_GEN2.0, LBC7	FET_NCH_LV_GEN2.0, LBC7

- QBS: Qual By Similarity
- Qual Device TPS53319DQP, TPS53355DQP, TPS56121BDQP is qualified at LEVEL2-260C
- Device TPS53319DQP, TPS53355DQP, TPS56121BDQP contains multiple dies.

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	Test Name / Condition	Duration	Qual Device: TPS53319DQP	Qual Device: TPS53355DQP	Qual Device: TPS56121BDQP
ED	Electrical Characterization, side by side	Per Datasheet Parameters	1/30/0	1/30/0	-
FLAM	Flammability (UL 94V-0)	Flammability/Method A	-	-	3/15/0
HAST	Biased HAST, 130C/85%RH	96 Hours	-	-	3/231/0
HTSL	High Temp Storage Bake 170C	420 Hours	-	-	3/231/0
PD	Physical Dimensions	(per mechanical drawing)	-	-	3/30/0
SD	Solderability	Pb Free Solder	-	-	3/75/0
TC	Temperature Cycle, -55/125C	700 Cycles	3/231/0	1/77/0	3/231/0
XRAY	X-ray	(top side only)	1/5/0	1/5/0	1/5/0

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

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